Hardware Engineer

Tia Battle

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📍 San Jose, CA

Professional summary

Experienced Hardware Engineer with over 7 years of experience in designing and testing circuit boards and developing system architecture. Adept at collaborating with cross-functional teams to ensure product specifications are met and systems are optimized for performance and cost.

Links

LinkedIn : /in/tiabattle

Education

Bachelor of Science in Electrical Engineering

University of California, Berkeley, CA

Graduated: May 2017

Experience

Hardware Engineer

June 2020 - Now

Intel Corporation / Santa Clara, CA

- Design and develop digital and analog circuit boards for consumer-grade electronics and enterprise systems.
- Lead the implementation of a high-speed memory interface for a cutting-edge processor, improving performance by 15%.
- Utilize AutoCAD and Altium Designer for schematic capture and PCB layout.
- Collaborate with embedded systems teams to ensure hardware and software integration.
- Perform hardware validation and troubleshooting, ensuring compliance with quality standards.

Junior Hardware Engineer

January 2018 - May 2020

Qualcomm Technologies / San Diego, CA

- Assisted in the design and testing of power management circuits for mobile devices.
- Created detailed circuit schematics and PCB designs for wireless communication systems.
- Conducted signal integrity analysis and thermal testing on prototype hardware.
- Worked alongside senior engineers to resolve hardware malfunctions in pre-production models.

Skills

Skill	Rating
PCB Design (Altium Designer, AutoCAD)	◆◆◆◆◆
FPGA Programming (VHDL, Verilog)	◆◆◆◆◆
Signal Integrity Analysis	◆◆◆◆◆
Power Systems Design	◆◆◆◆◇
Circuit Simulation and Testing (LTspice, Multisim)	◆◆◆◆◇
Communication Protocols (I2C, SPI, PCIe)	◆◆◆◆◆
Schematic Capture	◆◆◆◆◆

PROJECTS AND ACCOMPLISHMENTS

High-Speed Memory Interface Development

- Led the design and integration of a high-speed memory interface for Intel's latest processor, increasing data throughput by 15%, enabling smoother video processing capabilities.

Wireless Charging Technology

- Developed and tested a wireless charging circuit for mobile phones, improving efficiency by 20% over previous models.